

## Operational experience with the NA62 Gigatracker

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The Gigatracker is a hybrid silicon pixel detector developed for the NA62 experiment at CERN, which aims at measuring the branching fraction of the ultra-rare kaon decay  $K^+ \rightarrow \pi^+ \nu \bar{\nu}$  at the CERN SPS. The detector has to track particles in a 75 GeV/c hadron beam with a flux reaching 1.3 MHz/mm<sup>2</sup> and provide single-hit timing with better than 200 ps r.m.s. resolution for a total material budget of less than 0.5%  $X_0$  per station. The tracker comprises three 61 × 27 mm<sup>2</sup> stations installed in vacuum (about 10<sup>-6</sup> mbar) and cooled with liquid C<sub>6</sub>F<sub>14</sub> circulating through micro-channels etched inside few hundred of microns thick silicon plates. Each station is composed of a 200 μm thick planar silicon sensor bump-bonded to 2 × 5 custom 100 μm thick ASIC, called TDCpix. Each chip contains 40 × 45 asynchronous pixels, each 300 × 300 μm<sup>2</sup> and is instrumented with 720 time-to-digital converter channels with 100 ps bin. In order to cope with the high rate, the TDCpix is equipped with four 3.2 Gb/s serializers sending out the data. Detector description, operational experience and results from the NA62 experimental runs will be presented.

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## 1. Introduction

NA62 is a fixed target experiment at the CERN Super Proton Synchrotron, whose main objective is the measurement of branching fraction of the ultra-rare decay  $K^+ \rightarrow \pi^+ \nu \bar{\nu}$  with a statistical uncertainty of about 10% in a few years of data taking [1, 2].

The  $K^+ \rightarrow \pi^+ \nu \bar{\nu}$  is a flavour changing neutral current decays proceeding through box and electroweak penguin diagrams. A quadratic GIM mechanism and a strong Cabibbo suppression make this process extremely rare, hence extremely sensitive to physics beyond the Standard Model (SM), probing the highest mass scales among the rare meson decays. Using the value of tree-level elements of the Cabibbo-Kobayashi-Maskawa (CKM) triangle as external inputs, the SM predicts  $BR(K^+ \rightarrow \pi^+ \nu \bar{\nu}) = (8.4 \pm 1.0) \times 10^{-11}$  [3, 4]. The existing measurement  $BR(K^+ \rightarrow \pi^+ \nu \bar{\nu})_{exp} = (17.3^{+11.5}_{-10.5}) \times 10^{-11}$  is based on 7 events (E787/E949 experiments).

The NA62 experiment plans to collect about  $10^{13}$  kaon decays in few years using 400 GeV/c protons from SPS. In order to attain the required precision, a 10% signal acceptance and about 10% signal to background ratio are needed. The primary proton energy from the accelerator and the requests on kaon statistics and signal acceptance suggest the use of a kaon decay-in-flight technique similar to NA48.

The primary proton beam impinges on a beryllium target and produces plenty of secondary particles. Of those, only 75 GeV/c positively charged particles are selected in a narrow momentum band (about 1 %). The resulting beam intensity is very high (750 MHz), and the beam composition is such that only about 6% kaons are produced, the rest being pions and protons.

## 2. The Gigatracker detector system

In order to track the high intensity NA62 beam an innovative pixellated detector named Gigatracker [5] has been built, which is able to provide measurement of the momentum, time and angle of all beam tracks, and to sustain high and non-uniform particles rate. The additional requirement is that the detector should be as thin as possible in order to reduce multiple scattering and beam hadronic interactions to a minimum.

Kinematical rejection of background events require a momentum resolution of about 0.2% and angular resolution of  $\sim 16 \mu\text{rad}$ , which can be attained with a combination of three stations at a relative distance of about ten meters, with bending magnets arranged in an ‘‘achromat’’ configuration, and a pixel size of  $300 \times 300 \mu\text{m}^2$ . One of the most important requirements is the time resolution of better than 200 ps on single hits, which is needed to correctly associate the parent kaon track to the daughter decay particles. In addition, a total material budget of about 0.5%  $X_0$  has been fixed for the single station.

A prototype Gigatracker (GTK) detector has been produced and extensively studied via electrical, laser and particle beam tests. All the above requirements were successfully verified, and in particular a time resolution better than 150 ps has been measured at high over-depletion [6]. These results led to the development of the full-scale chip for the GTK detector, called TDCpix [7, 8]

One GTK station is made of a single sensor ( $61 \times 27 \text{ mm}^2$ ) bump-bonded to ten TDCpix read-out chips. The GTK stations dissipate about 35 W each and are installed in vacuum ( $\sim 10^{-6}$  mbar), therefore an efficient cooling system, and with very low material budget (about 0.15%  $X_0$  in the

active beam area) is necessary for stable detector operation. A micro-channel cooling solution has been adopted by the NA62 Collaboration for the first time in a high energy physics experiment [9, 10]. In addition, the detector is exposed to high and non-uniform radiation levels: the expected fluence is about  $2 \times 10^{14}$  1 MeV  $n_{eq}/\text{cm}^2$  during 100 days of operation in the sensor center, and a regular exchange of detectors is performed to mitigate performance degradation due to radiation damage.

## 2.1 Sensor, front-end read-out chip and micro-channel cooling plate

Sensors used for the GTK are planar 200  $\mu\text{m}$  thick p-on-n or n-on-p, produced by FBK (Trento, Italy) and CiS (Erfurt, Germany). An over-depleted operation of the detector of about 300 V is required to achieve the target time resolution.

Read-out wafer thinning to 100  $\mu\text{m}$  and flip-chip bonding to the sensor is done at IZM (Berlin, Germany), where also Benzocyclobutene (BCB) is deposited to prevent the risk of electric discharges between the sensor edges and the read-out chips.

As mentioned above, a specific read-out chip called TDCpix [7, 8] has been developed for the Gigatracker project. It is made of 40 columns of 45 pixels each for a total of 1800 pixels, for a total size of about  $12 \times 20 \text{ mm}^2$ . The chip is produced in IBM 130 nm CMOS technology and features an on-pixel fast preamplifier and discriminator with 65 mV/fC gain, 5 ns peaking time and an equivalent noise charge of 170 electrons (210 with bump-bonded sensor).

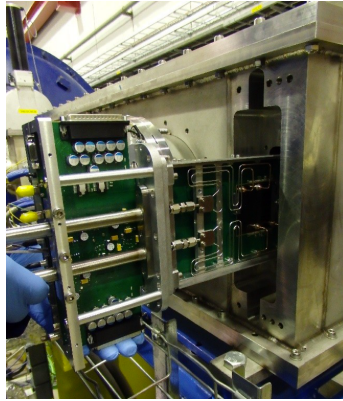
The architecture is data driven, and asynchronous transmission lines transfer discriminated signal from the pixels to the end-of-column logic, where digitization takes place. Digital signals from 5 pixels in a column are sent to a “hit arbiter”: a TDC pair is connected to each hit arbiter to measure leading and trailing edge times. 9 TDC pairs are used per column, for a total of 360 TDC pairs per chip that perform dual edge stamping with about 98 ps bins. Four 3.2 Gb/s serializers per chip provide the necessary bandwidth to deal with the maximum expected 210 MHits/s rate. In addition the digital logic is triplicated in order to protect for Single Event Upsets.

The cooling plate is fabricated by CEA Leti (Grenoble, France) etching channels and openings for inlets/outlets in a  $\sim 200 \mu\text{m}$  thick silicon plate glued to the GTK bump-bonded assembly. Liquid coolant ( $\text{C}_6\text{F}_{14}$ ) at a temperature down to  $-15^\circ\text{C}$  (at the detector inlet) is circulated in the  $200 \times 70 \mu\text{m}^2$  micro-channels at a pressure of 3.5 bars and a mass flow of 3 g/s, via two cooling circuits: fluid is brought in with capillaries and kovar connectors are soldered onto the cooling plate.

## 2.2 Electro-mechanical integration

The GTK bump-bonded assembly is glued on the micro-channel cooling plate. The cooling plate is clamped on the carrier Printed Circuit Board (PCB), then the TDCPix chips are wired bonded to the PCB (73  $\mu\text{m}$  bonding pitch on chip). The carrier PCB delivers power, clock and configuration signals to the TDCpix. In addition it receives data from the TDCpix chips through 40 differential 3.2 Gb/s lines over 30 cm.

The carrier PCB is then glued on a frame and a flange, which closes the vacuum, allowing easy access for detector maintenance or replacement.



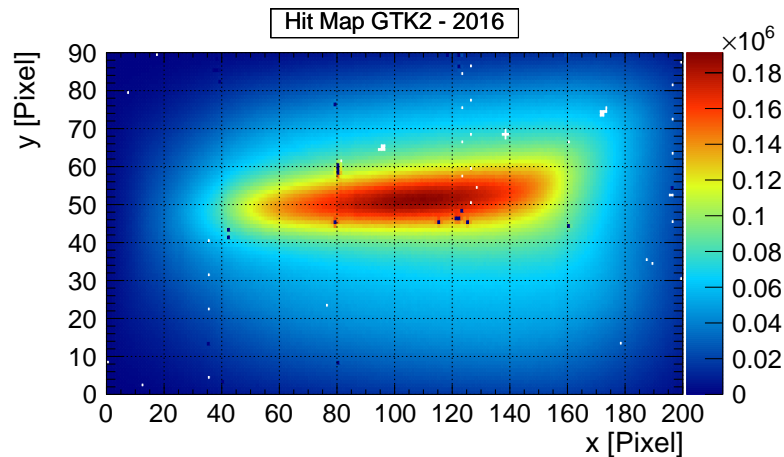
**Figure 1:** Installation of Gigatracker station 3 inside the vacuum vessel.

### 2.3 Back-end read-out system

As the TDCpix sends out every hit, a trigger matching needs to be implemented in the external back-end read-out system (GTK-RO), which is built using Altera Stratix GX110 FPGAs. Each GTK-RO is made of two decked cards, with a daughter card that handles the interface with Timing Trigger and Control (TTC) system of the experiment.

Each TDCpix is connected to one GTK-RO through 4 optical links (one per 3.2 Gb/s serializer) plus 1 configuration link. The trigger matching efficiency could be measured splitting the optical signals and reading out trigger-less data concurrently (on few boards for low beam intensity).

Each GTK-RO buffers data, waiting for a level-0 trigger decision within the maximum latency of 1 ms, retrieves data in a 75 ns time window upon trigger request, and finally sends data to the sub-detector PC.



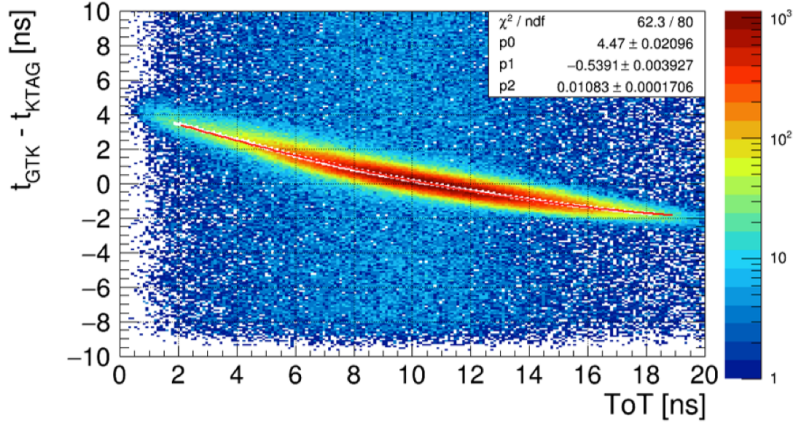
**Figure 2:** Particle distribution as a function of the pixel number in both horizontal (X) and vertical (Y) directions.

### 3. Gigatracker operation and performance

After production of single-chip and full assemblies with the final TDCpix chip, and character-

ization in the lab with electrical and laser tests, three detector prototypes of the final design were installed in 2014 in the NA62 experimental line (Fig. 1).

The Gigatracker detector system was fully commissioned in 2015, and operated at a temperature between  $-10^{\circ}\text{C}$  and  $0^{\circ}\text{C}$ , with bias voltages applied to the sensor ranging from 100 V to 300 V depending on the running periods.



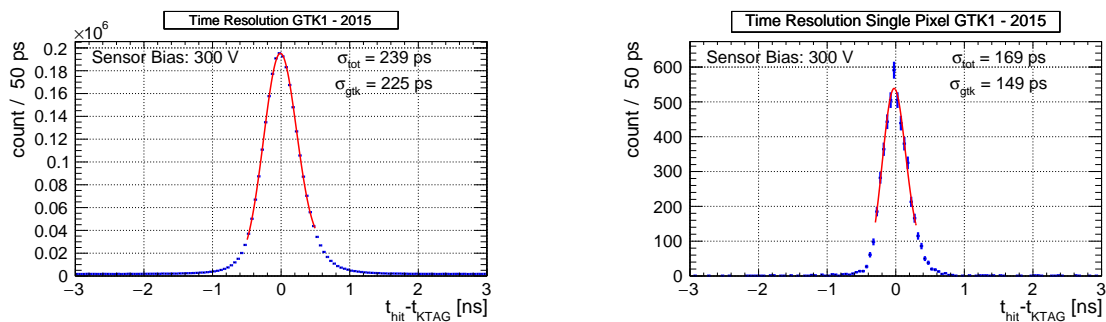
**Figure 3:** Plot of the time difference between GTK and KTAG as a function of the GTK time-over-threshold, from which the time-walk correction is extracted.

The NA62 beam line was commissioned up to the nominal intensity, while the sub-detectors and trigger and data acquisition systems were tested up to about 30% of the nominal beam intensity (as of September 2016).

In Fig. 2 one can observe the beam distribution at the Gigatracker station number 2. The number of non operational pixel (due either to missing bumps or noise) is at the per-mille level.

In order to correct for the time-walk effect, corrections are extracted using the Cherenkov detector called KTAG [11] as timing reference. In Fig. 3 is shown the time difference between GTK and KTAG versus the Time-over-Threshold (ToT) of the GTK hit: a fit to this distribution is done and the correction curve is then applied offline to the leading edge time to improve the time resolution.

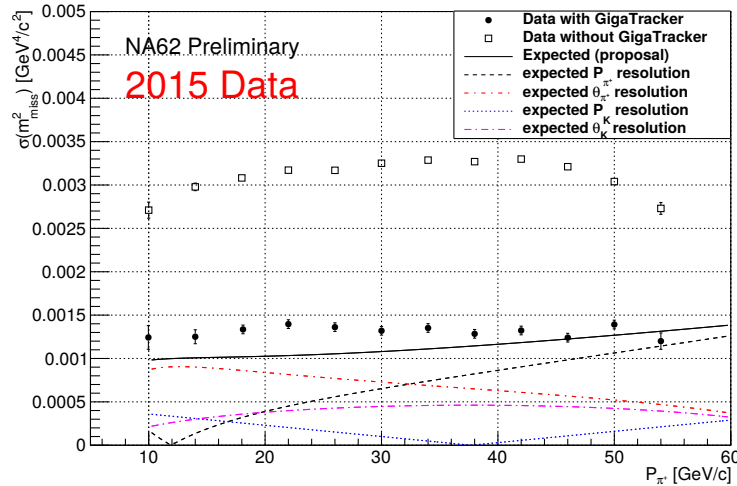
Preliminary results on the time resolution for 300 V applied bias are shown in Fig. 4. The time resolution is calculated by measuring the time difference between the GTK hits and the KTAG,



**Figure 4:** Time difference between the GTK and KTAG for all pixels (left) and a single pixel (right) of GTK station 1.

and is shown on the left plot for all pixels of Gigatracker station 1. The sigma of a Gaussian fit is shown as well, together with the value calculated subtracting in quadrature the KTAG contribution. On the right plot the time resolution for a single pixel is shown: this value is compatible with the one measured during the test beam of the prototype detector, and is currently much better than the value for the whole station. The lower time resolution of the left plot is due to the fact that the time walk and time offset corrections were less precise as they had to be derived for groups of pixels to get enough statistics. Further calibrations and data analysis are needed to exploit the full potential of the detector.

The performance of the GTK detector and its contribution to the kinematics suppression of background events has been measured using data taken in special conditions with a minimum bias trigger and low beam intensity during the 2015 run. In particular, the resolution on the squared missing mass  $(p_{K^+} - p_{\pi^+})^2$  was measured for  $K^+ \rightarrow \pi^+ \pi^0$  decays and is shown in Fig. 5 as a function of the  $\pi^+$  momentum, using information from the GTK or assuming the nominal beam momentum (75 GeV/c) travelling along the beam axis. These measurements are compared with the expected resolutions from simulations, and show the important contribution of the Gigatracker to improve the missing mass resolution by almost a factor three.



**Figure 5:** Squared missing mass resolution  $\sigma((p_{K^+} - p_{\pi^+})^2)$  as a function of the charged pion momentum for  $K^+ \rightarrow \pi^+ \pi^0$  decays, using GTK information or simply assuming the nominal beam momentum and direction [12].

## Conclusions

The Gigatracker is a thin hybrid pixel detector (about 0.5%  $X_0$  per station) with a very high density of channels (18000 per sensor), capable of measuring the NA62 beam particle tracks with a resolution better than 200 ps in vacuum. The Gigatracker detector system has been fully commissioned in the NA62 experimental run and is taking data since 2014 showing the expected resolutions and rate capability. The Gigatracker detector allows to greatly improve kinematical rejection for

background events, thus is an essential ingredient to precisely measure  $BR(K^+ \rightarrow \pi^+ \nu \bar{\nu})$ . An innovative and very-low mass cooling concept based on micro-channels etched on silicon plates is being used for the first time in a high energy physics experiment and allows stable operations of the detector in vacuum. The NA62 beam intensity has been increased regularly since the start of operations and has reached its nominal value at the end of the 2016 run (November). Analysis of the full 2015 and 2016 data sets is on-going. In particular, data from the last period of the 2016 run will allow to measure the degradation of detector performance due to radiation damage.

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